

Generalized grid-forming droop control for three-wire connection

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Abstract—In this work, we investigate generalized grid-forming (GFM) control for three-phase/three-wire dc/ac voltage source converters (VSCs) under unbalanced system conditions and faults. To fully leverage the degrees of freedom of VSCs with three-wire connection, we propose a generalized two-phase GFM control that combines individual GFM controls for two controllable phases with a phase balancing feedback and suitable three-wire implementations of the current saturation algorithm (CSA) and threshold virtual impedance (TVI). The proposed control enables current limiting for all three phases during unbalanced faults, reduces to positive sequence GFM droop control in balanced systems and is suitable to work both in stand-alone and grid-connected systems. High-fidelity simulations are used to illustrate the properties of the control.

I. INTRODUCTION

Inverter-based resources (IBRs) are being deployed on the power grid at an ever-increasing rate. Stand-alone IBRs (e.g., uninterruptible power supplies) are already widely used for homes, businesses, and data centers, and their penetration is expected to see further growth [1]. Meanwhile, increased renewable generation motivates widespread adoption of inverter controls that explicitly address practical issues such as unbalanced operating conditions and unbalanced fault ride through for both four-wire and three-wire systems. Primarily these controls can be divided into grid-forming (GFM) and grid-following control [2].

While grid-following (GFL) converters can provide some grid support functions [2], [3], they require other devices to form a stable ac voltage waveform that GFL converters can lock on to. The GFM converters however can impose a well-defined and stable ac voltage waveform at their point of connection [4]. GFM converters are advantageous during contingencies due to their faster dynamics. However, limiting current to protect their semiconductor switches is a major challenge, especially during unbalanced faults.

Works that consider GFM control of grid-connected IBRs under unbalanced faults (e.g., [5]) typically leverage symmetrical components, e.g., implementing separate GFM controls for positive and negative sequence. This approach is motivated by standard power system analysis methods for unbalanced faults using symmetrical components. However, the relationship between the converter phase currents and symmetrical components is highly nonlinear [5] resulting in challenging

control design and analysis problems. In contrast, [6] proposed to limit phase currents directly for voltage source converters (VSCs) with four-wire connection (e.g., grounded dc midpoint, wye-ground connected filter capacitors). While this approach allows GFM converters to maximize their degrees of freedom, the results in [6] do not directly apply to VSCs with three-wire connection commonly used for utility-scale IBRs.

In a three-wire connection, the LC output filter capacitors of VSC can be arranged in a wye configuration (see, e.g., [7]–[9]), or a delta configuration (see, e.g., [10]). Typically, in the literature, stand-alone uninterruptible power supply (UPS) systems with wye-connected output filter capacitors are considered and the focus is on supplying non-linear loads efficiently. A dual-loop Lyapunov-based control scheme is proposed in [9] which focuses on large signal stability of three-phase stand-alone inverters with improved robustness to nonlinear loads. Similarly, the work in [8] applies dual-loop control and utilizes double synchronous reference frame control. The work in [7] takes a different approach by applying multi-objective model predictive control (MPC) which finds analytical solution of a cost function tailored to the objective of generating optimal reference voltages for stand-alone VSCs. Despite being computationally expensive, none of these methods take fault current limiting into consideration in the design of their controls. A basic control scheme for delta-connected filter capacitors that does not account for the restrictions of three-wire connections is presented in [10].

In contrast, the GFM control for three-phase/three-wire grid-connected two-level dc/ac VSCs proposed in this work develops a control that leverages the capabilities of VSCs to control two line-to-line voltages to regulate all the output voltages and line currents such that, during unbalanced loading or faults, (i) the line current does not exceed the semiconductor current limits, and (ii) the line-to-line voltages are balanced whenever possible. To achieve this, we implement single-phase GFM control for two controllable phases and combine it with a balancing feedback that controls the trade-off between three-phase voltage unbalance and unbalance of the converter power injection between phases. We leverage the two-wattmeter method (see [11]) to develop a two-phase GFM droop control. The GFM voltage references are then tracked by an inner voltage and current control that separately control the two controllable VSC phases. The currents of two phases are controlled to implicitly control the third phase current to limit all three of them during unbalanced faults. This control architecture fully leverages the remaining degrees of freedom of three-wire connected VSCs (see [12] for details on current limits of three-wire connected VSC). To limit

This material is based upon work supported by the U.S. Department of Energy's Office of Energy Efficiency and Renewable Energy (EERE) under the Solar Energy Technologies Office Award Number 38637. The views expressed herein do not necessarily represent the views of the U.S. Department of Energy or the United States Government. D. Groß and P. Bhagwat are with the Department of Electrical and Computer Engineering at the University of Wisconsin-Madison, USA; e-mail: {dominic.gross,pbhagwat2}@wisc.edu.

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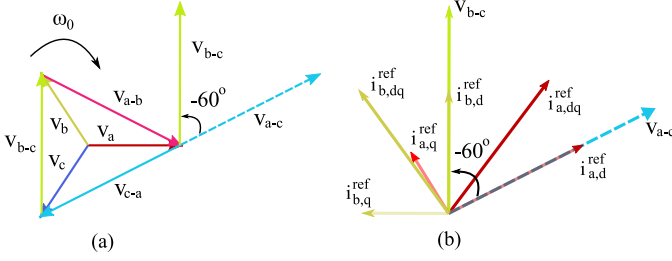


Fig. 2. Line-to-line and line-to-neutral voltage phasors (a) and current phasors with respect to line-to-line voltage phasors in a dq frame with angles $\theta_{a-c}^{\text{gfm}}$ and $\theta_{b-c}^{\text{gfm}}$ (b).

Considering $(\theta_{a-c}^{\text{bal}}, \theta_{b-c}^{\text{bal}}) := (0, -\frac{\pi}{3})$ (see Fig. 2 (a)) and voltage setpoints V_{p-c}^* , the GFM voltage angle and magnitude references $\theta_{p-c}^{\text{gfm}} = \delta_{p-c}^{\text{gfm}} + \theta_{p-c}^{\text{bal}}$, where $\frac{d}{dt}\theta_{p-c}^{\text{gfm}} := \omega_{p-c}$, and $V_{p-c}^{\text{gfm}} = V_{\delta, p-c}^{\text{gfm}} + V_{p-c}^*$ for $p \in \{a, b\}$ respectively are determined by the outer GFM control in per unit as

$$\frac{d}{dt}\delta_{a-c}^{\text{gfm}} = \omega_0 - k_P(\delta_{a-c}^{\text{gfm}} - \delta_{b-c}^{\text{gfm}}) + m_P(\Delta P_1), \quad (5a)$$

$$\tau \frac{d}{dt}V_{\delta, a-c}^{\text{gfm}} = -V_{\delta, a-c}^{\text{gfm}} - k_Q(V_{\delta, a-c}^{\text{gfm}} - V_{\delta, b-c}^{\text{gfm}}) + m_Q(\Delta Q_1), \quad (5b)$$

$$\frac{d}{dt}\delta_{b-c}^{\text{gfm}} = \omega_0 - k_P(\delta_{b-c}^{\text{gfm}} - \delta_{a-c}^{\text{gfm}}) + m_P(\Delta P_2), \quad (5c)$$

$$\tau \frac{d}{dt}V_{\delta, b-c}^{\text{gfm}} = -V_{\delta, b-c}^{\text{gfm}} - k_Q(V_{\delta, b-c}^{\text{gfm}} - V_{\delta, a-c}^{\text{gfm}}) + m_Q(\Delta Q_2). \quad (5d)$$

where $\Delta P_n := P_n^* - P_n$ and $\Delta Q_n := Q_n^* - Q_n$ for $n \in \{1, 2\}$ are the deviations of the converter output powers from their setpoints P_n^* and Q_n^* . Moreover, $k_P \in \mathbb{R}_{\geq 0}$ is the balancing feedback of $P-f$ droop, $k_Q \in \mathbb{R}_{\geq 0}$ is the balancing feedback of $Q-V$ droop. Then, the line-to-line GFM voltage reference is defined by $\angle v_{p-c, dq}^{\text{gfm}} = \theta_{p-c}^{\text{gfm}}$ and $\|v_{p-c, dq}^{\text{gfm}}\| = V_{p-c}^{\text{gfm}}$ for $p \in \{a, b\}$.

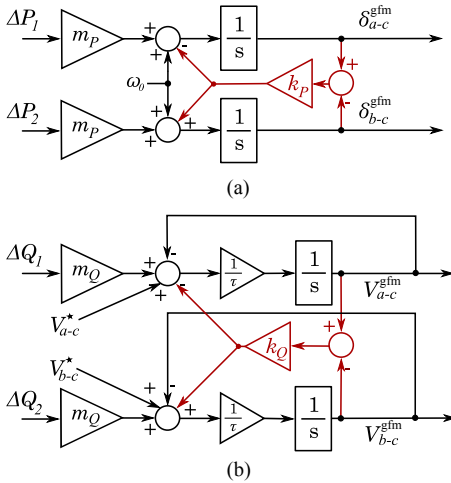


Fig. 3. $P-f$ (a) and $Q-V$ (b) droop control structure of generalized two-phase GFM control for three-wire connection. The balancing feedback (red) controls the difference between phase voltage magnitudes and phase angles.

The balancing of the two droop controllers in (5) through the balancing feedback with gain k_P and k_Q is shown in Fig. 3 (a) and Fig. 3 (b). Specifically, if k_P and k_Q are zero, the droop equations (4) reduce to two individual $P-f$ and $Q-V$ droop controls, which may result in three-phase unbalanced GFM reference voltages under unbalanced load. For instance,

if $\Delta P_1 \neq \Delta P_2$, then the voltage phase angle differences may deviate from 120° . In contrast, if k_P and k_Q are increased, then the balancing feedback reduces the phase imbalance of the GFM reference voltage. Ultimately, as $k_P \rightarrow \infty$ and $k_Q \rightarrow \infty$, the voltage reference of two-phase GFM droop control in (5) converges to positive-sequence GFM droop control.

B. Dual-loop voltage and current control with current limiting

The reference voltage $v_{p-c, dq}^{\text{ref}}$ in dq-frame for $p \in \{a, b\}$ (e.g., generated by (5)) are tracked using dual-loop vector current and voltage proportional-integral (PI) controllers. To this end, for $p \in \{a, b\}$, the voltage PI controller $G_{\text{PI}}(s)$ is used to compute the filter current reference $i_{p, dq}^{\text{ref}} \in \mathbb{R}^2$, i.e.,

$$i_{p, dq}^{\text{ref}} = i_{o, p, dq} + Y_f v_{p-c, dq} + G_{\text{PI}}(s)(v_{p-c, dq}^{\text{ref}} - v_{p-c, dq}). \quad (6)$$

Here, $Y_f = \omega_0 R(\frac{\pi}{2})c_f$ denotes the filter admittance matrix, $v_{p-c, dq}^{\text{ref}}$ denotes the voltage reference, and $i_{o, p, dq} \in \mathbb{R}^2$ denotes the output current of the converter. Next, $i_{p, dq}^{\text{ref}}$ is passed to inner current PI loop which is implemented for $p \in \{a, b\}$ as

$$v_{\text{sw}, p-c, dq} = v_{p-c, dq} + Z_f i_{p, dq} + G_{\text{PI}}(s)(\rho_p^{\text{lim}} i_{p, dq}^{\text{ref}} - i_{p, dq}), \quad (7)$$

where $Z_f = r_f \mathbb{I}_2 + \omega_0 R(\frac{\pi}{2})l_f$ denotes the filter impedance matrix ($\mathbb{I}_2$ is 2×2 identity matrix), $v_{\text{sw}, p-c, dq} \in \mathbb{R}^2$ is the line-to-line switching voltage modulated by the VSC, $i_{p, dq} \in \mathbb{R}^2$ is the filter current of the converter, and $\rho_p^{\text{lim}} \in \mathbb{R}_{>0}$ is a multiplier used for current limiting. From $v_{\text{sw}, a-c, dq}$ and $v_{\text{sw}, b-c, dq}$ we obtain their respective time domain signals $v_{\text{sw}, a-c}(t)$ and $v_{\text{sw}, b-c}(t)$ by transforming from their respective rotating dq frame to stationary frame using $\theta_{a-c}^{\text{gfm}}$ and $\theta_{b-c}^{\text{gfm}}$ from (5). Then, the line-to-line switching voltages are computed by,

$$v_{\text{sw}, a-b}(t) = v_{\text{sw}, a-c}(t) - v_{\text{sw}, b-c}(t). \quad (8)$$

We pass all three line-to-line voltages to a delta-star transformation (assuming floating neutral) to obtain $v_{\text{sw}, a}(t)$, $v_{\text{sw}, b}(t)$ and $v_{\text{sw}, c}(t)$ which are used to compute gate signals using standard pulse-width modulation algorithms.

Using the above controls, current limiting of $i_{p, dq}^{\text{ref}}$ to the maximum current I_{max} can either be implemented through (i) the well-known current saturation algorithm (CSA) and multiplier ρ_p^{lim} (see Sec. IV-C) or, (ii) by modifying the voltage reference $v_{p-c, dq}^{\text{ref}}$ through threshold virtual impedance (TVI) [13] (see Sec. IV-D).

C. Unbalanced CSA for three-wire connection

The voltage reference with CSA is given by $v_{p-c, dq}^{\text{ref}} = v_{p-c, dq}^{\text{gfm}}$ and CSA limits $i_{p, dq}^{\text{ref}}$ for $p \in \{a, b\}$ as

$$i_{p, dq}^{\text{lim}} := \rho_p^{\text{ref}} i_{p, dq}^{\text{ref}}, \quad (9)$$

where $\rho_p^{\text{ref}} := \min(1, \frac{I_{\text{max}}}{\|i_{p, dq}^{\text{ref}}\|})$ and $I_{\text{max}} \in \mathbb{R}_{>0}$ is the maximum phase current magnitude. In other words, ρ_p^{ref} is used to limit the filter currents i_a and i_b when using the CSA. To implement CSA for i_c we need to control $i_{a, dq}^{\text{lim}}$ and $i_{b, dq}^{\text{lim}}$. The filter current $i_c(t)$ can be obtained from $i_c(t) = -i_a(t) - i_b(t)$ where $i_a(t), i_b(t), i_c(t) \in \mathbb{R}$ are instantaneous filter current measurements. Recall that the filter current reference $i_{p, dq}^{\text{ref}}$ computed from (6) is in dq frame with respect to $\theta_{p-c}^{\text{gfm}}$, i.e.,

$i_{a,dq}^{\text{ref}} = R(\theta_{a-c}^{\text{gfm}})[i_a^{\text{ref}}, i_a^{\text{ref},\perp}]^T$ and $i_{b,dq}^{\text{ref}} = R(\theta_{b-c}^{\text{gfm}})[i_b^{\text{ref}}, i_b^{\text{ref},\perp}]^T$, where filter reference current i_p^{ref} and $i_p^{\text{ref},\perp}$ are stationary frame α and β components respectively, discussed in Sec. III for $p \in \{a, b\}$. This is also illustrated in the Fig. 2 (b) where the d axis phasor of $i_{a,dq}^{\text{ref}}$ and $i_{b,dq}^{\text{ref}}$ is aligned to the v_{a-c} and the v_{b-c} phasors respectively. Therefore, we first transform $i_{b,dq}^{\text{ref}}$ to the frame of $i_{a,dq}^{\text{ref}}$ and then obtain $i_{c,dq}^{\text{ref}}$ as

$$i_{c,dq}^{\text{ref}} = -i_{a,dq}^{\text{ref}} - R(\theta_{b-c}^{\text{gfm}} - \theta_{a-c}^{\text{gfm}})[i_{b,d}^{\text{ref}}, i_{b,q}^{\text{ref}}]^T. \quad (10)$$

Here, $i_{c,dq}^{\text{ref}}$ is in a dq frame rotating with the frequency ω_{a-c} . When $\|i_{c,dq}^{\text{ref}}\| > I_{\text{max}}$, then, because $i_{c,dq}^{\text{ref}}$ is not directly controllable, $i_{p,dq}^{\text{lim}}$ for $p \in \{a, b\}$ needs to be modified to limit $i_{c,dq}^{\text{ref}}$. To this end, we define limiting multiplier

$$\rho_p^{\text{lim}} := \rho_c^{\text{ref}} \rho_p^{\text{ref}} \quad (11)$$

for $p \in \{a, b\}$. Notably, the multiplier $\rho_c^{\text{ref}} := \min(1, \frac{I_{\text{max}}}{\|i_{c,dq}^{\text{ref}}\|})$ has a similar interpretation to ρ_a^{ref} and ρ_b^{ref} .

To avoid harmonic injections, the CSA implemented in (9) and (11) does not clip the current waveform but instead adjusts the magnitude of the reference current. Moreover, the angle of the reference current is preserved.

D. Unbalanced TVI for three-wire connection

Another well-known current limiting method, which results in reduced transients during post-fault recovery, is threshold virtual impedance (TVI). When TVI is used instead of CSA, it holds that $\rho_p^{\text{lim}} = 1$ for $p \in \{a, b\}$. Instead, the voltage drop

$$v_{p,dq,\text{TVI}} = \begin{bmatrix} R_{p,\text{TVI}} & X_{p,\text{TVI}} \\ -X_{p,\text{TVI}} & R_{p,\text{TVI}} \end{bmatrix} \begin{bmatrix} i_{p,d}^{\text{ref}} \\ i_{p,q}^{\text{ref}} \end{bmatrix}. \quad (12)$$

over a virtual impedance with resistance $R_{p,\text{TVI}}$ and reactance $X_{p,\text{TVI}}$ for each phase $p \in \{a, b, c\}$ is used for current limiting. To implement the unbalanced TVI (12) for a three-wire connection, (12) is used to compute the line-to-neutral voltage drop over the TVI which is subsequently converted to line-to-line voltage drops that are subtracted from the GFM reference voltages to obtain $v_{p-c,dq}^{\text{ref}}$ in dq -frame for $p \in \{a, b\}$.

To this end, given the filter current reference magnitude $\|i_{p,dq}^{\text{ref}}\|$ we define the virtual resistance $R_{p,\text{TVI}} = \max(0, k_{\text{TVI}}\|i_{p,dq}^{\text{ref}}\| - I_{\text{th}})$ with TVI gain $k_{\text{TVI}} \in \mathbb{R}_{>0}$ and threshold $I_{\text{th}} \in \mathbb{R}$. Moreover, the virtual reactance is given by the TVI reactance-to-resistance ratio $X_{\text{TVI}}/R_{\text{TVI}}$ and $X_{p,\text{TVI}} = X_{\text{TVI}}/R_{\text{TVI}}R_{p,\text{TVI}}$. In other words, the virtual impedance increases if the filter current magnitude exceeds the threshold I_{th} . We first obtain $i_{c,dq}^{\text{ref}}$ from (10), then we calculate the TVI-induced line-to-neutral voltage drop $v_{p,dq,\text{TVI}}$ per phase as (12). We then convert this line-to-neutral TVI voltage drop $v_{p,dq,\text{TVI}}$ into a line-to-line voltage drop. To this end, recall that $i_{c,dq}^{\text{ref}}$ is already aligned with the v_{a-c} phasor, hence $v_{c,dq,\text{TVI}}$ is also aligned with the v_{a-c} phasor. Moreover, we change the frame of $v_{c,dq,\text{TVI}}$ in (13b) from the v_{a-c} phasor to the v_{b-c} phasor before calculating $v_{b-c,dq,\text{TVI}}$. Thus, the TVI-induced line-to-line voltage drop is given by

$$v_{a-c,dq,\text{TVI}} = v_{a,dq,\text{TVI}} - v_{c,dq,\text{TVI}}, \quad (13a)$$

$$v_{b-c,dq,\text{TVI}} = v_{b,dq,\text{TVI}} - \mathcal{R}(\theta_{a-c}^{\text{gfm}} - \theta_{b-c}^{\text{gfm}}) \begin{bmatrix} v_{c,d,\text{TVI}} \\ v_{c,q,\text{TVI}} \end{bmatrix}. \quad (13b)$$

Finally, we assign the voltage reference in (6) as

$$v_{p-c,dq}^{\text{ref}} = v_{p-c,dq}^{\text{gfm}} - v_{p-c,dq,\text{TVI}}. \quad (14)$$

V. CASE STUDY

We use an EMT simulation of a switching model of a two-level dc/ac VSC with delta-connected filter capacitors and floating dc bus, connected to a transmission system modeled as a double-circuit transmission line and infinite bus. The system and parameters are shown in Fig. 4. The VSC is connected to a 1 km medium voltage feeder through a transformer and the (closed) breaker S_1 . The medium voltage feeder connects to a 40 km double-circuit high voltage transmission line through a step-up transformer. The generalized two-phase GFM control ($m_P = m_Q = 5\%$) is implemented at a sampling rate of 10 kHz. The switching frequency of the VSC is also set at 10 kHz.

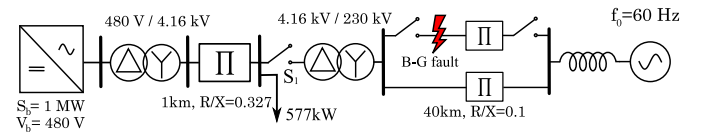


Fig. 4. Test system with a low-voltage VSC connected to a weak ac system through step-up transformers, a medium-voltage feeder, and a double-circuit transmission line.

Initially, the system is in a balanced steady state and the power setpoints $P_1^* = P_2^* = 0.45$ p.u. result in a total active power output of the GFM converter of $P_1^* + P_2^* = 0.9$ per unit. To demonstrate the proposed GFM control ability to limit current during fault, we simulate a single-line-to-ground fault (phase b to ground) at $t = 3$ s that is cleared after ten cycles at 60 Hz. Note that, before the fault, the system is balanced and hence all phase quantities are identical. Line-to-line voltage magnitudes are shown in Fig. 5, while the currents, active powers, and reactive powers shown in Fig. 5 are phase filter currents and powers. The current and the voltage magnitudes shown in Fig. 5 are computed using the maximum value in each ac cycle to avoid artifacts introduced by, e.g., the Hilbert transform (1). In contrast, the active and reactive powers shown are filtered estimates for the phase powers obtained by applying a Fourier Transform to extract their 60 Hz components, i.e., remove double frequency components.

The response obtained using CSA is shown in the left column of Fig. 5 while results obtained with TVI are shown in the right column. Using CSA, the current of phase c immediately reaches $I_{\text{max}} = 1.2$ p.u. after fault inception and the magnitude of v_{b-c} significantly drops once the current limit has been reached. The TVI threshold has been selected as $I_{\text{th}} = 1.1$ p.u., $X_{\text{TVI}}/R_{\text{TVI}} = 1$ for all $p \in \{a, b, c\}$ and $k_{\text{TVI}} = 4.4$. Due to the proportional response of the TVI, the current of phase c is limited at approximately 1.1 p.u. (i.e., less than $I_{\text{max}} = 1.2$ p.u.).

Once the fault is cleared after ten ac cycles at $t = 3.166$ s by disconnecting the affected line segment, Fig. 5 shows resynchronization transients in the GFM converter response. Using CSA, the converter exhibits significant resynchronization transients for voltage, active power, and reactive power. Moreover, the settling time to the post-fault equilibrium is

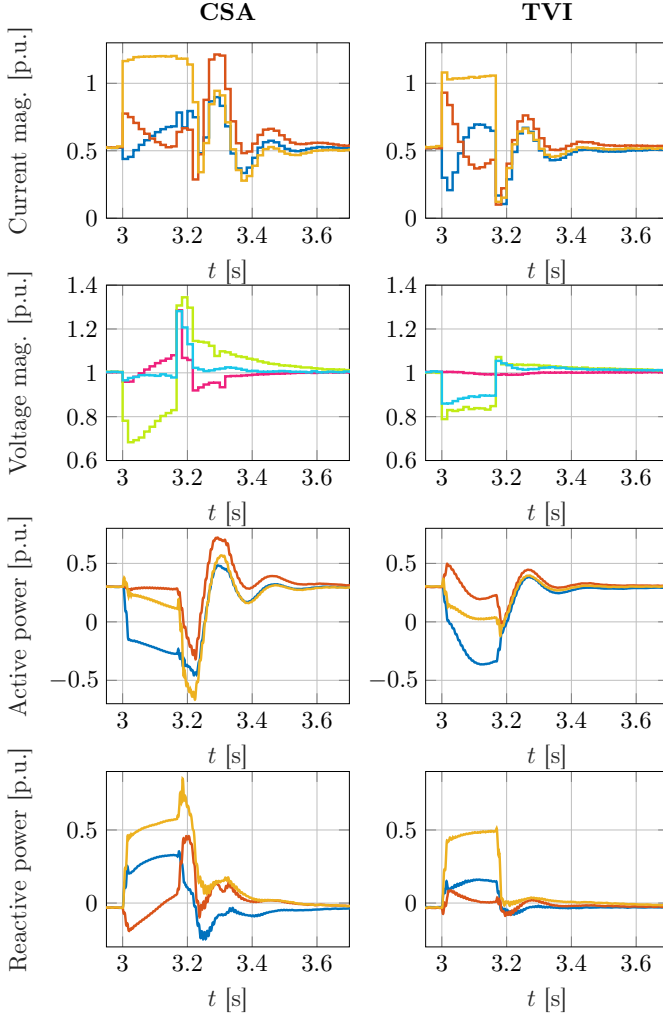


Fig. 5. Response of GFM converter to a phase b to ground fault on a transmission line between $t = 3$ s and $t = 3.166$ s. Voltage magnitudes are shown for line-to-line voltages v_{a-b} (—), v_{b-c} (—), v_{c-a} (—). Current magnitudes and estimated instantaneous powers are shown for phase a (—), b (—), c (—). The base values are $S_b = 1$ MW and $V_b = 480$ V (see Fig. 4).

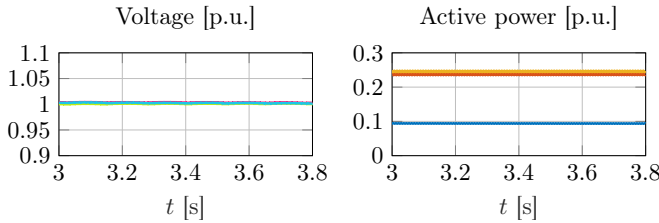


Fig. 6. Response of GFM converter supplying an unbalanced 577 kW load while islanded. The plot colors and base values are identical to Fig. 5

significant. In contrast, when TVI is used, the settling time is shorter and the transients are significantly reduced, which can be attributed to the TVI reducing integrator wind-up in the voltage loop. Nevertheless, using TVI, the GFM converter can inject 91% of its maximum current $I_{\max}$. Additionally, we also test the stand-alone capability of the GFM converter by connecting 580 kW of highly unbalanced linear load and opening breaker S_1 . In this case, the converter is able to supply

the load while maintaining balanced line-to-line voltages as shown in Fig. 6.

VI. CONCLUSION

This paper considered GFM control for three-phase dc/ac voltage source converters with three-wire connection. To this end, this work proposed a GFM droop control that combines individual GFM controls for two controllable phases with a phase balancing feedback. Moreover, the voltage references generated by the generalized two-phase outer GFM control are tracked by inner dual-loop current and voltage controls for each phase that enable direct limiting of phase currents without clipping of current waveforms. In contrast to previous works in literature, this approach maintains full control of the VSC terminal voltage and enables effective limiting of phase currents through current saturation and virtual impedance during unbalanced faults. A high-fidelity case study is used to demonstrate the effectiveness of the proposed control under unbalanced medium-voltage loads and unbalanced faults.

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